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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/598,514	01/29/2007	Chuen Khiang Wang	P26634	6698
	7590 09/09/201 & BERNSTEIN, P.L.	EXAMINER		
	CLARKE PLACE	AHMED, SELIM U		
KESTON, VA	20191		ART UNIT	PAPER NUMBER
			2826	
			NOTIFICATION DATE	DELIVERY MODE
			09/09/2010	ELECTRONIC

Please find below and/or attached an Office communication concerning this application or proceeding.

The time period for reply, if any, is set in the attached communication.

Notice of the Office communication was sent electronically on above-indicated "Notification Date" to the following e-mail address(es):

gbpatent@gbpatent.com pto@gbpatent.com

Advisory Action Before the Filing of an Appeal Brief

Application No.	Applicant(s)		
10/598,514	WANG ET AL.		
Examiner	Art Unit		
SELIM AHMED	2826		

	SELIM AHMED	2826	
The MAILING DATE of this communication appe	ars on the cover sheet with the c	correspondence add	ress
THE REPLY FILED <u>24 August 2010</u> FAILS TO PLACE THIS AF	PPLICATION IN CONDITION FOR	ALLOWANCE.	
1. The reply was filed after a final rejection, but prior to or on application, applicant must timely file one of the following application in condition for allowance; (2) a Notice of Apperfor Continued Examination (RCE) in compliance with 37 C periods:	replies: (1) an amendment, affidavit eal (with appeal fee) in compliance	t, or other evidence, w with 37 CFR 41.31; or	hich places the (3) a Request
a) The period for reply expires 3 months from the mailing date b) The period for reply expires on: (1) the mailing date of this A no event, however, will the statutory period for reply expire to Examiner Note: If box 1 is checked, check either box (a) or (MONTHS OF THE FINAL REJECTION. See MPEP 706.07(f)	dvisory Action, or (2) the date set forth in ter than SIX MONTHS from the mailing b). ONLY CHECK BOX (b) WHEN THE	g date of the final rejection	n.
Extensions of time may be obtained under 37 CFR 1.136(a). The date of have been filed is the date for purposes of determining the period of extunder 37 CFR 1.17(a) is calculated from: (1) the expiration date of the set forth in (b) above, if checked. Any reply received by the Office later may reduce any earned patent term adjustment. See 37 CFR 1.704(b).	ension and the corresponding amount of hortened statutory period for reply origin	of the fee. The appropria nally set in the final Office	ate extension fee e action; or (2) as
 The Notice of Appeal was filed on A brief in comp filing the Notice of Appeal (37 CFR 41.37(a)), or any exter Notice of Appeal has been filed, any reply must be filed with AMENDMENTS 	nsion thereof (37 CFR 41.37(e)), to	avoid dismissal of the	
3. The proposed amendment(s) filed after a final rejection, b	out prior to the date of filing a brief	will not be entered be	cause
(a) They raise new issues that would require further cor (b) They raise the issue of new matter (see NOTE below (c) They are not deemed to place the application in better appeal; and/or	nsideration and/or search (see NOT w); er form for appeal by materially rec	E below); ducing or simplifying th	
(d) They present additional claims without canceling a c NOTE: (See 37 CFR 1.116 and 41.33(a)).	corresponding number of finally reje	ected claims.	
4. The amendments are not in compliance with 37 CFR 1.12 5. Applicant's reply has overcome the following rejection(s):		mpliant Amendment (l	PTOL-324).
 Applicants reply has overcome the following rejection(s). Newly proposed or amended claim(s) would be all non-allowable claim(s). 		imely filed amendmer	nt canceling the
7. For purposes of appeal, the proposed amendment(s): a) [how the new or amended claims would be rejected is prov The status of the claim(s) is (or will be) as follows: Claim(s) allowed: Claim(s) objected to: Claim(s) rejected: 1.5-13.15-22.25 and 27-31. Claim(s) withdrawn from consideration: Claim(s) withdrawn from consideration:	☑ will not be entered, or b) ☑ will ided below or appended.	l be entered and an e	xplanation of
AFFIDAVIT OR OTHER EVIDENCE	. hafana an an tha data af filing a Nia		
 The affidavit or other evidence filed after a final action, but because applicant failed to provide a showing of good and was not earlier presented. See 37 CFR 1.116(e). 			
9. The affidavit or other evidence filed after the date of filing entered because the affidavit or other evidence failed to of showing a good and sufficient reasons why it is necessary	vercome <u>all</u> rejections under appea and was not earlier presented. Se	ıl and/or appellant fail: ee 37 CFR 41.33(d)(1	s to provide a).
10. The affidavit or other evidence is entered. An explanation	n of the status of the claims after er	ntry is below or attach	ed.
REQUEST FOR RECONSIDERATION/OTHER 11. ☑ The request for reconsideration has been considered but See continuation Note.	does NOT place the application in	condition for allowan	ce because:
12. Note the attached Information <i>Disclosure Statement</i> (s). (13. Other:	PTO/SB/08) Paper No(s)		
/Sue A. Purvis/ Supervisory Patent Examiner, Art Unit 2826			

On page 2 of the remark filed on 8/24/2010 applicant argued, "The Examiner has taken the position that, in the LI device, since the dielectric material is a flowable material that is curable and that the dielectric material is stuck between the elements, then the dielectric material 35 is an adhesive layer as claimed. However, Applicants respectfully submit that this position is incorrect and inappropriate in this case, since the discreet and separate masses of bonding material 30, which are not in a layer, comprise the adhesive that adheres the members together. The dielectric material, which is in a layer, does not adhere the members together. In this regard, contrary to the Examiner's assertions, merely being a flowable, curable material does not mean that the dielectric material is an adhesive layer. Additionally, merely being positioned (or stuck) between two surfaces does not mean that the dielectric material adheres the two surfaces together. Accordingly, a flowable, curable material that is positioned between two surfaces does not, as in this case, necessarily adhere two surfaces together. Therefore, in the LI device, the material that is an adhesive is not provided in a layer; and the material that is provided in a layer is not an adhesive. Accordingly, contrary to the Examiner's contention, LI does not teach or suggest the first adhesive layer, as claimed."

Applicant's above arguments have been fully considered but found not persuasive. First of all, applicant's specification did not disclose any specific type of material of adhesive layer. According to chamber 21st century dictionary (2010 credo reference), adhesive is defined as, sticky; able to make things stick together; or any substance that is used to bond two surfaces together. So, any sticky layer or layer that able to make things stick together; or layer with any substance that is used to bond to surfaces together can be used to meet the claim limitation. As indicated in the rejection, para[0038] of Li discloses dielectric material 35 may be formed from a flowable, curable polymer. Li's layer 35 is sticking between chip 28 and substrate 10. Furthermore, it is known in the art that polymer material having at least some degree of adhesive properties as it fills the space between chip and packaging substrate and sticks between chip and substrate after curing. For example, US 2003/0209801 discloses an underfill 167 polymer adhesive (para[0022]) that fills the space between die 160 and package substrate 170. It is important to note that US 2003/0209801 clearly discloses polymer adhesive implying polymer material having adhesive properties. Since polymeric material 35 stick between chip 28 and substrate 10, it is reasonable to say that 35 is an adhesive layer. It appears that Li's flowable dielectric material 35 is used as an underfill, sticking between chip 28 and substrate 10. The material 35 is only discontinuous in solder balls region but still forms a layer between the chip 24 and substrate 10 in remaining other region than the solder balls region. Secondly, in Fig.9 (different embodiment) of Li discloses a layer (non-labeled) between chip 124 and substrate 110. Since the (non-labeled) layer stick between the chip 124 and the substrate, it can be said as an adhesive layer as well. Thirdly, the secondary Issak reference discloses an epoxy (please note that epoxy is a kind of polymer) layer 126 between a chip 108 and flexible substrate 104 as well. Since epoxy layer 126 is polymer that has adhesive properties, it is reasonable to say that layer 126 acts as an adhesive layer. So, the examiner still believes that Li's layer 35 between the substrate 10 and the chip 28 is an adhesive layer and epoxy layer 126 in secondary reference, Issak is also acting as an adhesive layer and so the final rejection sent on 5/26/2010 is proper.